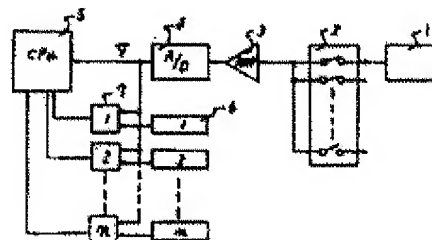


FAILURE DIAGNOSTIC SYSTEM FOR ANALOG-TO-DIGITAL CONVERTER**Publication number:** JP57196619**Publication date:** 1982-12-02**Inventor:** FUJII KAZUHIRO**Applicant:** HITACHI LTD**Classification:****- international:** H03M1/10; H03M1/10; (IPC1-7): H03K13/02**- European:** H03M1/10**Application number:** JP19810080954 19810529**Priority number(s):** JP19810080954 19810529[Report a data error here](#)**Abstract of JP57196619**

PURPOSE: To obtain the information at which stage a failure is decided in case of failure, by stepwise setting a setting range of each register through the provision of a plurality of comparison registers.

CONSTITUTION: The failure diagnosis for an analog/digital converting operation, by fetching a reference voltage of a reference voltage generator 1 and comparing the value with a value of a preset comparison register 6. In this case, a plurality of registers 6 and comparators 7 are provided. Thus, a level discrimination for a failure can be made by setting stepwise a compared value to each register 6.



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